

Important:

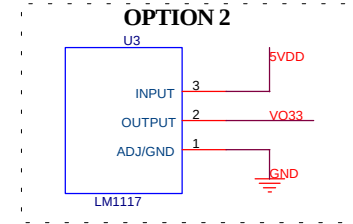
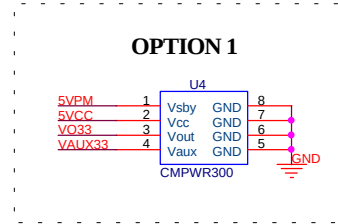
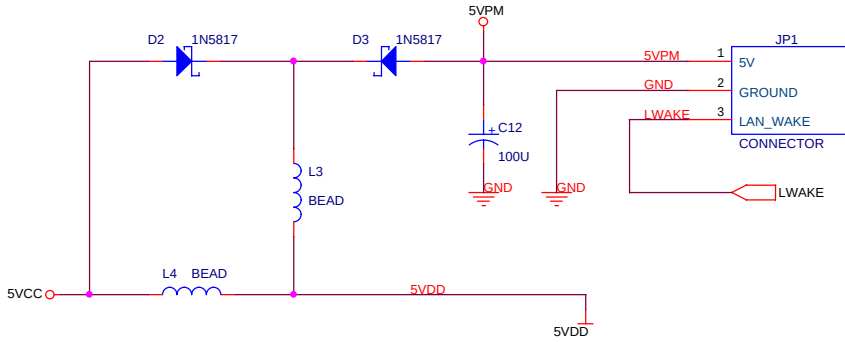
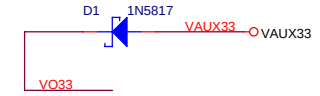
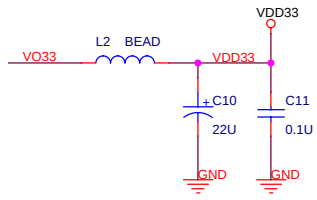
*For PCI 2.1,2.2 combo application, remove L4

*For PCI 2.2 only application, remove D2,D3,JP1,L3,C12

*For non Wake On LAN application, remove D1,D2,D3,JP1,L3,C12

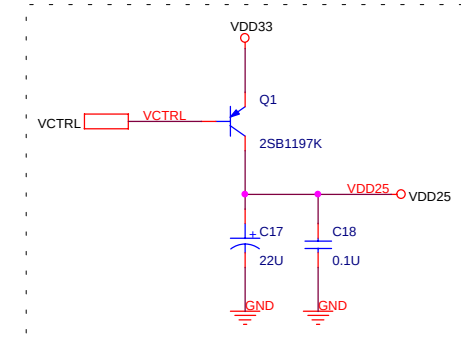
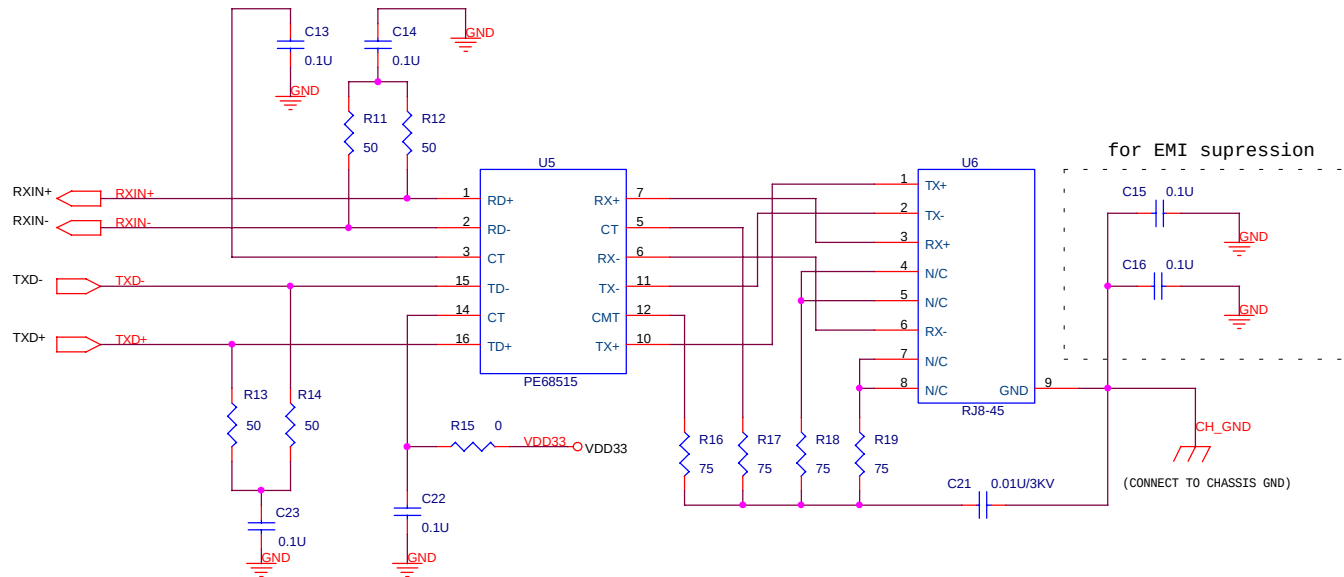
*For lump VAUX, C12 must be large enough to avoid power supply's compatibility

*For RTL8100 application, all bead must be rated 300mA/100ohm@100MHz



*Option 1 and option 2 are used for 5V -> 3.3V power.

Using option 1 could remove D1,D2,D3,L3 and L4.



The components of this region are reserved for future use. Please reserve the component space and pads in your layout for future extension.

Title		
Realtek RTL8100 application circuit -- LAN Card		
Size	Document Number	Rev
B	RED8100	1.01
Date:	Tuesday, November 21, 2000	Sheet 2 of 3